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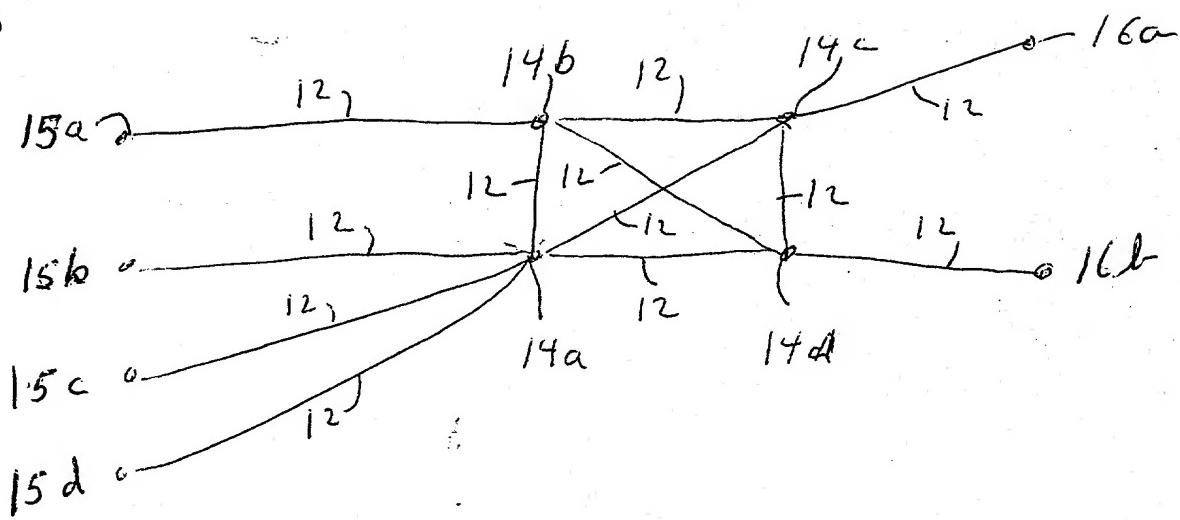


Fig. 1

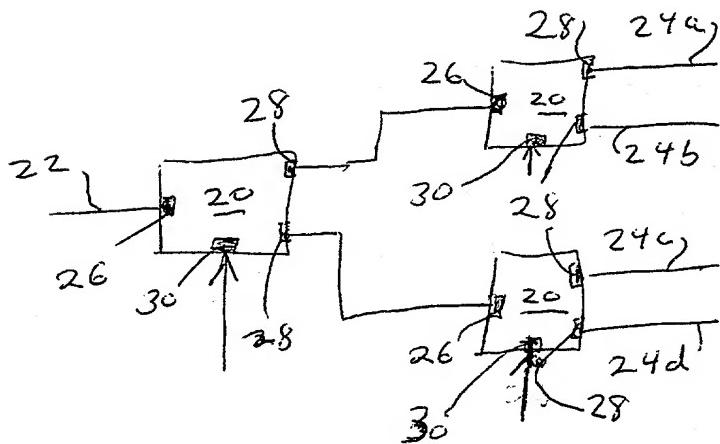


Fig. 2

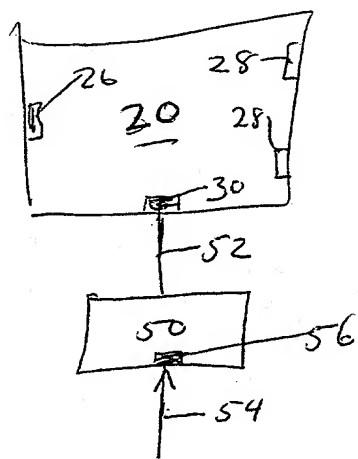


Fig. 3

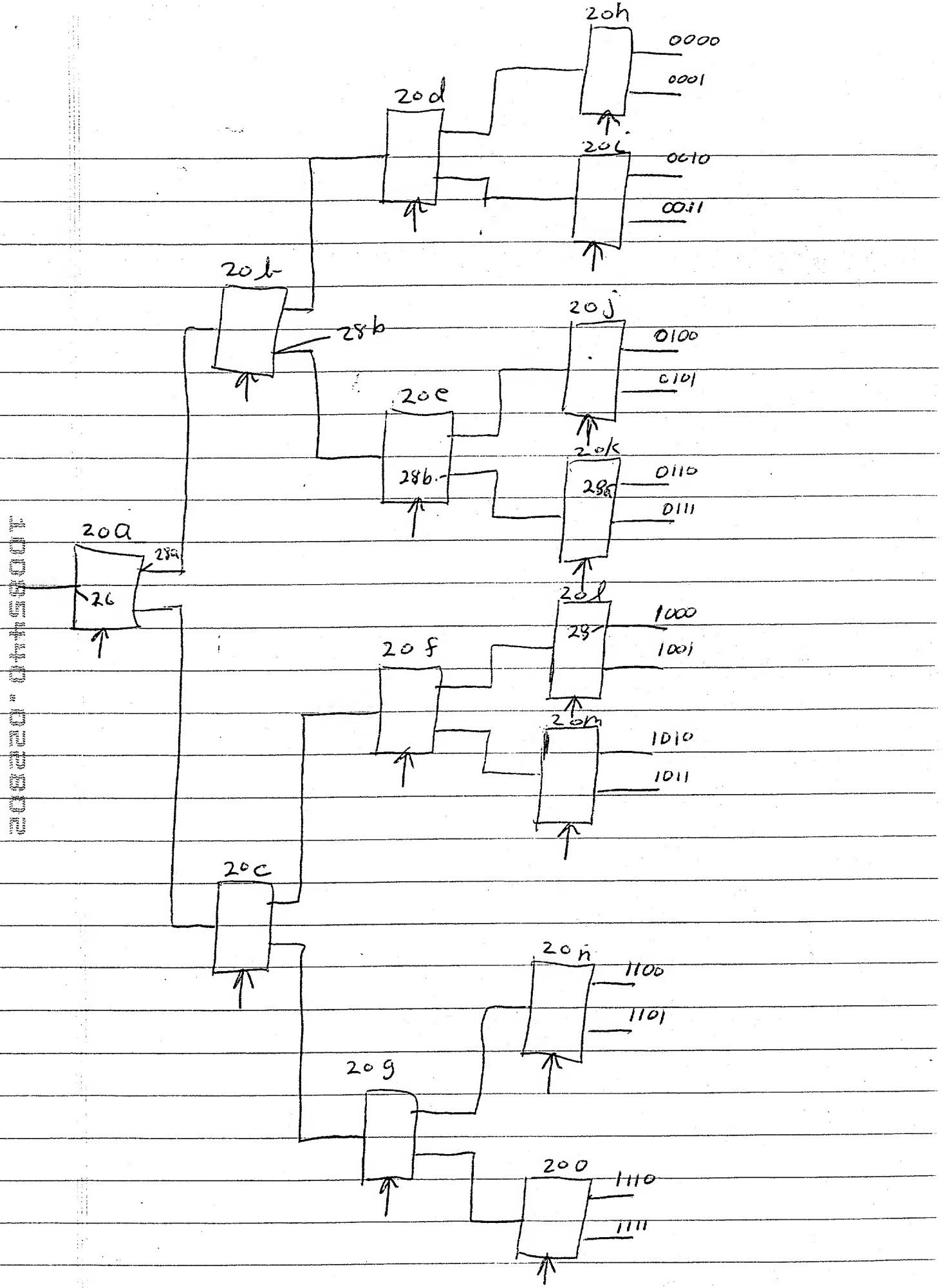


Figure 4

a b c d e f g h i j k l m n o

0000	1	1	x	1	x	x	x	1	x	x	x	x	x	x
0001	1	1	x	1	x	x	x	0	x	x	x	x	x	x
0010	1	1	x	0	x	x	x	x	1	x	x	x	x	x
0011	1	1	x	0	x	x	x	x	0	x	x	x	x	x
0100	1	0	x	x	1	x	x	x	x	1	x	x	x	x
0101	1	0	x	x	1	x	x	x	x	0	x	x	x	x
0110	1	0	x	x	0	x	x	x	x	1	x	x	x	← 54
0111	1	0	x	x	0	x	x	x	x	0	x	x	x	
1000	0	x	1	x	x	1	x	x	x	x	x	1	x	x
1001	0	x	1	x	x	1	x	x	x	x	0	x	x	
1010	0	x	1	x	x	0	x	x	x	x	x	x	1	x
1011	0	x	1	x	x	0	x	x	x	x	x	0	x	x
1100	0	x	0	x	x	x	1	x	x	x	x	x	1	x
1101	0	x	0	x	x	x	1	x	x	x	x	x	0	x
1110	0	x	0	x	x	x	0	x	x	x	x	x	x	1
1111	0	x	0	x	x	x	0	x	x	x	x	x	x	0

"1" - top port

"0" - bottom port

"x" - "don't care"

Figure 5

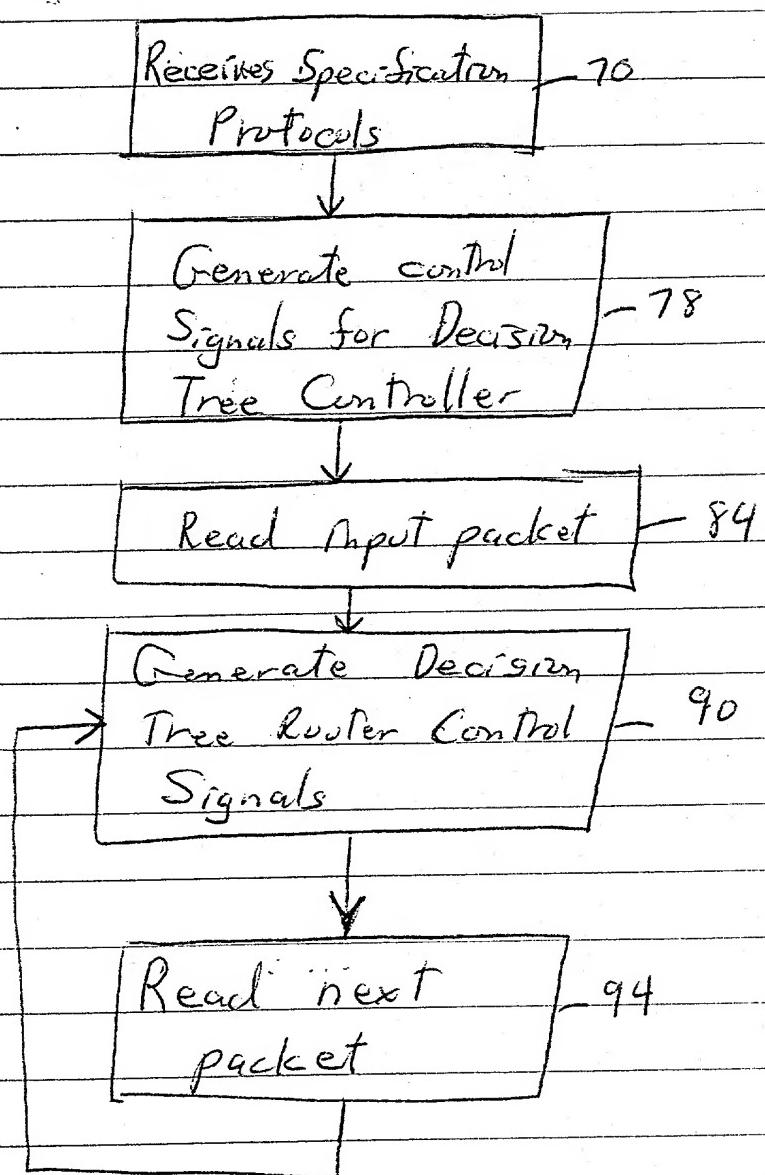


Fig 6

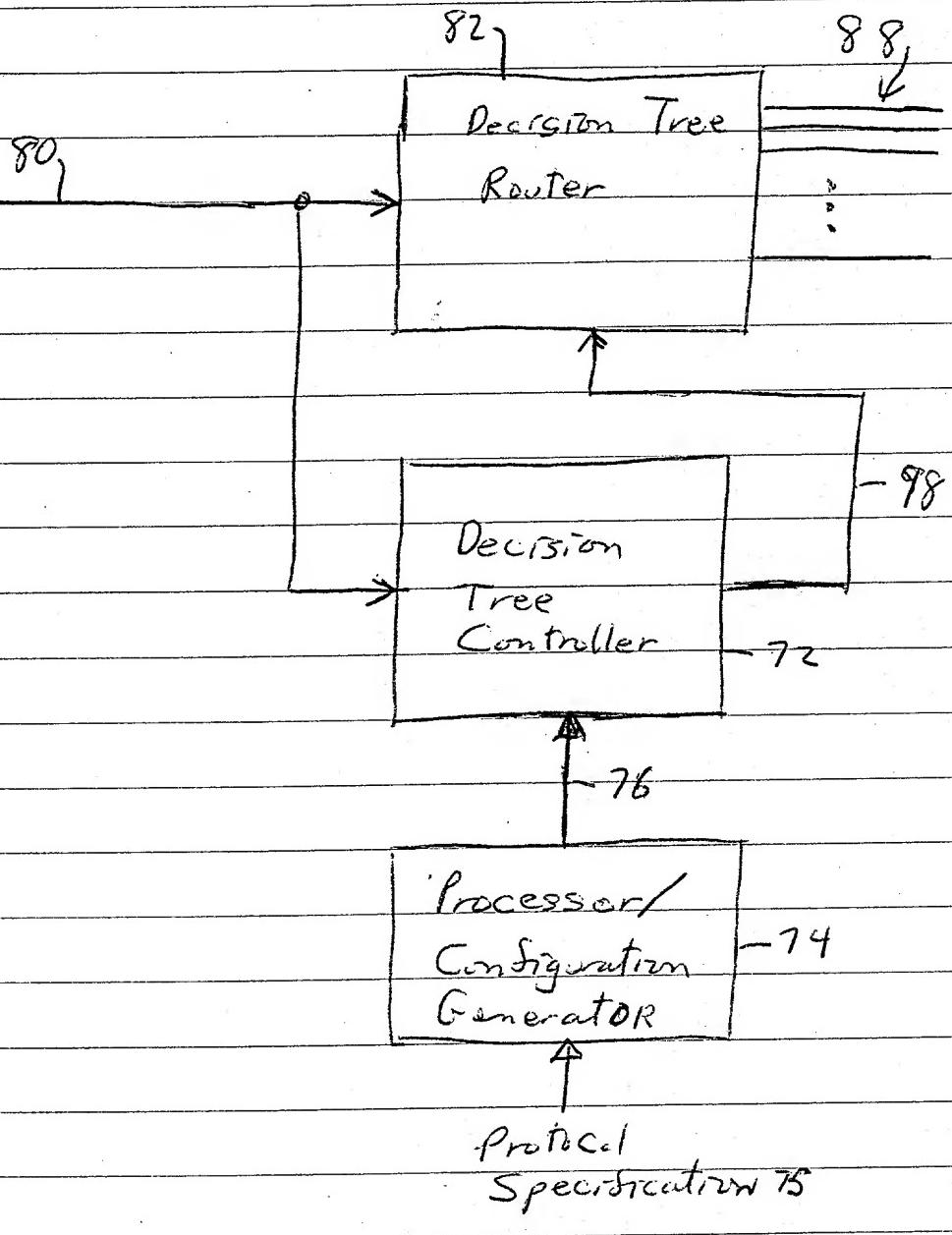


Fig. 7